

## 1 Supported platforms

### 1.1 General

The products

- > CANopen Slave Source Code (including the extensions MiniMaster and Layer Setting Services)
- > CANopen Master Source Code

are shipped together with a target adaptation. A target adaptation makes sure that the CANopen software components are prepared to run out of the box on the target. A target is usually a combination of a specific microcontroller with an internal or external CAN controller and a compiler.

In some cases there is also an operating system support included in the package. If this is the case it is stated in the following tables.

### 1.2 CANopen Master – target support

Microcontroller family/series; (derivative); [target operating system]	Compiler
<b>Freescall</b>	
S12; (MC9S12DP256)	CodeWarrior V3.x
PowerPC MPC5xx; (MPC555, MPC565)	GCC V3.2 series
PowerPC MPC52xx; (MPC5200B); [QNX]	GCC V3.2 series
<b>Infineon</b>	
C166; (C164CI, C167CR)	Tasking V8.x
XC166; (XC164CS/CM)	Tasking V8.x
<b>Renesas(Mitsubishi)</b>	
SH7040, SH-2 family (SH7047)	HEW V4.00 (C-Compiler V9.00)
SH7050, SH-2E family (SH7055)	HEW V4.00 (C-Compiler V9.00)
<b>ST Microelectronics</b>	
ST10; (ST10F276)	Keil V5, Tasking V8
<b>Vector Informatik</b>	
<b>XL-Interface-Family</b> ; [Windows XP, Vista, Windows 7]	MS Visual C++ 2005
<b>External CAN controller (may require target specific adaptation work)</b>	
NXP SJA1000	ANSI-C
Microchip MCP 2515	ANSI-C

Table 1-1 CANopen Master – supported targets

### 1.3 CANopen Slave – target support

Microcontroller family/series; (derivative); [target operating system]	Compiler
<b>Atmel</b>	
AVR® 8-Bit RISC; (AT90CAN128)	AVR-GCC V3.4.x, V4.2.x
8051 architecture; (T89C51CC01)	Keil C51 V7.x
AT91SAM7A; (AT91SAM7A3)	IAR Embedded Workbench V5.10.x
AT91SAM7X; (AT91SAM7X256)	IAR Embedded Workbench V5.10.x
AT91SAM9; (AT91SAM9263)	IAR Embedded Workbench V5.0
<b>Freescale</b>	
S12; (MC9S12DP256)	CodeWarrior V3.x
S12X; (9S12XDP512)	CodeWarrior V4.x
PowerPC MPC5xx; (MPC555, MPC565)	GCC V3.2 series
PowerPC MPC5xx; (MPC5554)	Green Hills MULTI
PowerPC MPC52xx; (MPC5200B)	GCC V3.2 series
ColdFire MCF52xx; (MCF5282)	CodeWarrior
Digital Signal Controller DSP56F80x; (DSP56F807)	CodeWarrior
Digital Signal Controller MC56F83xx; (MC56F8323)	CodeWarrior
<b>Fujitsu</b>	
16LX - MB90347/352/367/387/495/540 series	Softune Workbench V3
16FX - MB96340/350 series	Softune Workbench V3
FR50; (MB91F364)	Softune Workbench V3
<b>Infineon</b>	
C500; (C505C, C515CA)	Keil C51 V7.x
C166; (C164CI, C167CR)	Tasking V8.x
XC166; (XC164CS/CM)	Tasking V8.x
XE166; (XE164F)	Tasking VX
AUDO-NextGeneration; (TC1796)	Tasking VX, GCC V3.6 (Hitec)
<b>Linux - Open source environment</b>	
(socket-can); [Linux – generic, kernel version > 2.6.28]	GCC
<b>Microchip</b>	
PIC18; (PIC18Fxx8, PIC18Fxx8x)	MPLAB C Compiler V3.x
dsPIC30; (dsPIC30F6014)	MPLAB C Compiler V3.x
dsPIC33 DSC; (dsPIC33FJ256)	MPLAB C Compiler V3.x
<b>NEC</b>	
V85x; (V850ES/FE2, / FX3)	IAR Embedded Workbench V3.6A
V850E/A1	IAR Embedded Workbench V3.6A

Microcontroller family/series; (derivative); [target operating system]	Compiler
<b>NXP</b>	
LPC2000; (LPC2294, LPC2378)	IAR Embedded Workbench V5.40, GCC V4.4 series
<b>Renesas</b>	
M16C; (M16C6N, M16C29)	HEW V5.44 (C-Compiler V5.31)
R8C; (R8C23)	HEW V5.44 (C-Compiler V5.31)
M32C; (M32C80/87)	HEW V5.40 (C-Compiler V5.00)
H8SX; (H8SX/1527R)	HEW V4.00 (C-Compiler V6.1)
H8S; (H8S2612/23/26/36/38)	HEW V4.00 (C-Compiler V6.1)
SH7040, SH-2 family; (SH7047)	HEW V4.00 (C-Compiler V9.00)
SH7050, SH-2E family; (SH7055/58)	HEW V4.00 (C-Compiler V9.00)
SH7750, SH-4 family; (SH7760)	HEW V4.00 (C-Compiler V9.00)
<b>Silicon Laboratories</b>	
C8051F04X	Keil C51 uVision V7
<b>ST Microelectronics</b>	
ST10; (ST10F276)	Keil V5, Tasking V8
STR7; (STR710, STR712)	GCC 4.4
STR9; (STR910)	GCC 4.4
STM32; (STM32F10x)	IAR Embedded Workbench V5.40
<b>Texas Instruments</b>	
TMS320; (TMS320F2812, TMS320F28335)	TI CodeComposer V3.1
TMS320; (TMS320F2407A)	TI CodeComposer V3.1
<b>Vector Informatik</b>	
<b>XL-Interface-Family</b> ; [Windows XP, Vista, Windows 7]	MS Visual C++ 2005
<b>External CAN controllers (may require target specific adaptation work)</b>	
NXP SJA1000	ANSI-C
Microchip MCP 2515	ANSI-C

Table 1-2 CANopen Slave – supported targets

#### 1.4 Target support in a **project scope**

We also provide adaptation services for your specific target environment. In the past we have successfully integrated our components in different environments:

- > QNX (MPC5200B)
- > Green Hills INTEGRITY (MPC55xx)
- > Windows CE (NXP SJA1000, Microchip MCP 2515)
- > Wind River VxWorks (MPC5200B)
- > Micrium  $\mu$ C/OS-II (AT91SAM9263)
- > Linux (MPC5200B, PXA270 with NXP SJA1000)

## 2 Contact

Visit our website for more information on

- > News
- > Products
- > Demo software
- > Support
- > Training data
- > Addresses

[www.vector.com](http://www.vector.com)

[www.canopen-solutions.com](http://www.canopen-solutions.com)